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APPLICANTS Takaki Yoshida, Osaka, JAPAN; Reisuke Shimoda, Osaka, JAPAN;					
** CONTINUING DATA *****					
** FOREIGN APPLICATIONS ***** JAPAN 11-307872 10/29/1999					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 12/21/2000					
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged Examiner's Signature _____ Initials _____		STATE OR COUNTRY JAPAN	SHEETS DRAWING 50	TOTAL CLAIMS 52	INDEPENDENT CLAIMS 5
ADDRESS 27890					
TITLE Fault detecting method and layout method for semiconductor integrated circuit					
FILING FEE RECEIVED 2696	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		